



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Hall et al.

**Serial No.:** 10/785,122

**Filed:** February 24, 2004

**For:** UNDERFILLED, ENCAPSULATED  
SEMICONDUCTOR DIE ASSEMBLIES  
AND METHODS OF FABRICATION

**Confirmation No.:** 1653

**Examiner:** J. Diaz

**Group Art Unit:** 2815

**Attorney Docket No.:** 2269-5165.1US  
(01-0947.01/US)

**NOTICE OF EXPRESS MAILING**

Express Mail Mailing Label Number: EV669816599US

Date of Deposit with USPS: July 28, 2006

Person making Deposit: Wendy Neff

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Mail Stop Issue Fee  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

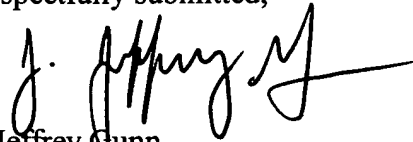
The Examiner indicates:

the prior art fails to teach, disclose, or suggest, either alone or in combination, a semiconductor assembly comprising a semiconductor substrate attached to a carrier substrate with a plurality of mutually spaced elements disposed in an area of overlapped between a surface of the semiconductor substrate and an opposing surface of the carrier substrate and a plurality of wire bonds extending between the semiconductor substrate and the carrier substrate; and a volume of dielectric filler material disposed between adjacent mutually spaced elements in the area of overlap, wherein:

- a. The semiconductor substrate is attached to the carrier substrate primarily by the volume of the dielectric filler material; or
- b. The dielectric filler material fills a majority of the area of overlap (e.g. the standoff volume) as instantly claimed, and in combination with the additional limitations.

Applicants concur with the reasons as stated by the Examiner insofar as they comprise a summary, and are exemplary and not limiting. However, the independent claims as allowed include other and different language than that specified by the Examiner, and the allowed dependent claims include other and further features and elements. Accordingly, the scope of the claims must be determined from the literal language of each as a whole, as well as equivalents thereof.

Respectfully submitted,



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Telephone: 801-532-1922

Date: July 28, 2006  
JJG/dlm:slm

Document in ProLaw



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(01-0947.01/US)

**Notice of Allowance Mailed:**

May 4, 2006

**NOTICE OF EXPRESS MAILING**

Express Mail Mailing Label Number: EV669816599US

Date of Deposit with USPS: July 28, 2006

Person making Deposit: Wendy Neff

**TRANSMITTAL LETTER**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

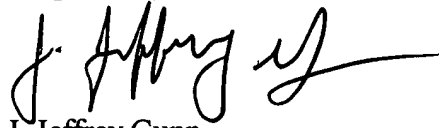
Applicants submit herewith Part B - Fee(s) Transmittal for the above-captioned application and a check in the amount of \$1,715.00 in payment therefor plus five (5) copies of the patent when issued.

**Serial No. 10/785,122**

Also enclosed are Amendment Pursuant to 37 C.F.R. § 1.312(a); Comments on Statement of Reasons for Allowance; and Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees.

Applicants understand that no additional fees are required. However, if the Office determines that any comparison fees or other additional fees are required, the Commissioner is authorized to charge any such fees to TraskBritt Deposit Account No. 20-1469. A copy of this Transmittal Letter is enclosed for deposit account charging purposes.

Respectfully submitted,



J. Jeffrey Gunn  
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Telephone: 801-532-1922

Date: July 28, 2006

JJG/dlm:slm

Enclosures: Part B - Issue Fee Transmittal

Check No. 23031 in the amount of \$1,715.00

Copy of Transmittal Letter

Amendment Pursuant to 37 C.F.R. § 1.312(a) (14 pages)

Comments on Statement of Reasons for Allowance (2 pages)

Fee Addressee for Receipt of PTO Notices Relating to Maintenance Fees (2 pages)

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